**TASK-8**

**TEAM-3**

**Introduction**

Automotive industry can't treat safety as an afterthought. Innovations like the airbag, antilock braking systems (ABS) and electronic stability programs (ESP) have helped to reduce the number of traffic fatalities and severity of accidents. But increase in the number of vehicles on the road, more than these technologies are needed to address a further reduction of accidents and fatalities. In the last few years Advanced Driver Assistance System (ADAS) features like radar- or camera-based systems have been introduced to make driving safer. ADAS constantly keep an eye on the road and makes sure to alert the driver in real-time of an impending danger. The increased use of complex automotive electronics systems requires that they should be designed for ultrareliability because the failure of an automotive system could place the vehicle's passengers in a life-threatening situation.

**How is the market evolving?**

Government legislation and strong consumer interest in safety features is one of the driving factors contributing to the ADAS market growth. Increased safety awareness and the desire for more driving comfort on the consumer side targets market to grow at a CAGR of 22.59 percent over the period 2012-2016. (1) Innovations in remote sensors and associated processing algorithms that extract and interpret critical information also fuel an increase in DA system deployment.

In recent years, the Global ADAS market has also been witnessing the increasing deployment of ADAS in low-cost cars. So the market is on the threshold of enormous growth. In the race to develop reliable and cost-effective ADAS, designers are presented with challenges to integrate functionality, develop scalable platforms and design systems that are robust enough to work in various operating conditions. In order to process multiple algorithms simultaneously, develop a scalable architecture, and get to market on time, ADAS system designers are increasingly turning to FPGAs and PSoCs to solve their challenging problems. The architecture of PSoCs or FPGAs is ideally suited for vision processing applications that require both fine grained parallelism and high-level processing. Automakers and suppliers benefit from IP (intellectual property) and other development aids available to help accelerate time-to-market and reprogram products to meet changing requirements and specifications.

**What are the challenges and opportunities?**

ADAS are expensive and this could pose a challenge to the growth of this market. Suppliers failed to offer extensions to the mandated technology that the consumer will continue to pay extra for. Suppliers need to offer a scalable platform that allows cost-effective deployment with the time to market and low risk in mind. Intellectual Properties used must be certified to the appropriate automotive safety (3) integrity level. To comply with ISO 26262 , an ADAS supplier must establish procedures associated with safety standards. The industry lacks interoperability specifications for radar, laser, and video data in the car network.

Also l acks standards for embedded vision processing algorithms. ADAS cannot add to driver distraction. Power dissipation is a crucial parameter since systems are either located behind the windshield in front of the rear-view mirror with direct exposure to sunlight or in the bumper in front of the radiator hence cannot dissipate much heat. Data transactions on external DDR memory especially can consume a substantial amount of power. Performance and power requirements can be a challenge when using general-purpose CPU architectures with often many cores running at high frequency. In addition, the algorithms used to process video and radar data combined with other data sources makes it difficult for automotive suppliers to determine fixed high-performance architecture. In order to handle the increased performance requirement, either the image resolution needs to be decreased, or the frame rate reduced or multiple devices need to be used. Therefore ability to add new algorithms to offer more features is also limited by a fixed computing architecture. Automotive suppliers face significant challenges to develop a base computing platform that can be quickly modified and scaled to meet the cost and performance targets in entry level and high-end luxury vehicles around the world.

**How FPGA can benefit from these challenges and opportunities?**

For the development of ADAS Systems, car makers require a common vehicle platform which can be reconfigured according to market needs since it saves the time-to-market and reduces cost of implementation while enhancing flexibility during manufacturing. FPGAs provide a suitable platform for developing the rapidly evolving ADAS Systems in automobiles. Most important requirements of ADAS Includes Higher levels bandwidth and performance for processing video streams from multiple cameras Complex, real-time processing required to combine different sensor inputs Transmit, receive, and translate between multiple communication standards such as CAN, MOST, Ethernet, LVDS.

**Meeting ADAS requirements**

FPGAs provide an ideal platform for developing low-power, low-cost, high-performance, ADAS systems with the most favourable level of integration and flexibility. FPGAs are reprogrammable. If there is any change in the processing architecture, it is possible to reprogram the hardware blocks in FPGA. Using FPGA, any changes can incorporate late in the design cycle. Using reprogrammable nature of FPGA, ADAS can support mutually exclusive functions using same FPGA. FPGAs are well suited to meet the various processing requirements of an ADAS. Two or more distinctly different processes can be run in parallel on a single FPGA. The features like Rear View Camera, Rear cross path etc can be implemented through video processing, manipulation and graphics rendering while the feature effective Pedestrian Detection is performed through image processing analytics. Another feature of FPGAs is device scalability. To add a new functionality to a serial DSP or an ASSP based system, it requires a complete re-architecture of the software design, even after moving to a more powerful device in the family. But in the case of an FPGA-based implementation, a new functional block can be added, utilizing previously unused FPGA logic and keeping the existing blocks as it is.

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**Benefits of FPGA based DA Systems compared to previous systems**

Power: The efficient implementation of data processing algorithm in FPGA reduces the power consumption compared to general-purpose computer architecture even though FPGAs consume considerable power generally.  

Performance: of different functionalities Parallel processing can be easily achieved with FPGAs. It is difficult to run different processes simultaneously with an ASSP based design. But it is possible on a single FPGA.

Functional partitioning of parallel and serial DA processes are possible. Functions that benefit from parallel processing are implemented in FPGA logic, while those more suited for serial processes are implemented in software. In FPGAs serial processes can be implemented either through Soft processors like Micro blaze (Xilinx) or through hard processors like ARM based Cortex –A9 processor. is a major advantage

Reprogrammable nature of FPGA systems over the other systems.

Reprogram ability is not possible with other alternatives.  FPGAs are scalable and Device scalability: flexible to support a wide range of design requirements for ADAS applications. Designers can use a device that is priced and sized appropriately for the specified feature set.

FPGAs meet external bandwidth requirements. FPGA memory controllers provide customized external memory interface design options to meet DA bandwidth needs and optimize all aspects of the cost equation.

Availability of block RAMs. on chip memory resources like Several FPGAs

**High-speed serial interfaces:** offer differential I/O that can operate at high speeds for the serial transport of data from external interfaces to the processing modules. It is possible to leverage these high-speed I/O capabilities along with the FPGA logic to implement emerging LVDS SerDes signaling protocols within the FPGA device itself, eliminating external components and reducing system cost.

**Functional safety**: ADAS need to meet specific functional safety requirements. ISO26262 Quality certified FPGA devices, IP, Development tools, and FPGA design flow ensures functional safety of the DA System.

**Better time to market**: IP cores are readily available for various sensor processing applications especially for video and image processing. Because of the readily available quality certified IP Cores and FPGA device scalability, time to market for FPGA based DA systems are less compared to other alternatives.

**Less cost of implementation**: Cost of implementation for FPGA based DA systems are less compared to others because of features like IP reuse, reprogram ability, device scalability, better integration or bundling of different features etc.

**Product obsolescence**: While designing DA Systems, designers must consider life span of hardware components used. FPGA life cycles span up to 15 years which is longer than application-specific integrated circuits (ASICs) and ASSPs.

**Issues that can impact FPGA selection**

**Firm-error immunity**: FPGAs depends on SRAM (static RAM) for configuration memory which makes them prone to neutron-induced errors which can cause thousands of failures in time. Moreover, these errors rise exponentially with fluctuations in temperature and altitude. But, Non-volatile flash memory solutions do not suffer from neutron-induced errors and are therefore firm-error immune.

**Electromagnetic interference (EMI)**: EMI is a major concern in the case of FPGA selection also. But, in reprogrammable FPGAs, EMI can be quickly eliminated.

**Speed:** ADAS applications require lightning-fast response times. FPGA response times are usually very low, may be several orders of magnitude faster than even a high-performance microcontroller. These fast response times help to reduce both EMI and power consumption.

**Conclusion**

Automatic driver assistance system (ADAS) is one of the fastest growing segments in automobile industry. In ADAS, sensors and algorithms are combined to understand the vehicle environment so that the driver can receive assistance or be warned of potential hazards. The processing platform requirements for an efficient ADAS system are architectural flexibility, platform scalability, external memory bandwidth, on-chip memory resources, highspeed serial interfaces, and parallel/serial process partitioning, functional safety. FPGA technology provides a suitable platform for such DA systems and is a viable alternative to standard ASSP and ASIC approaches.